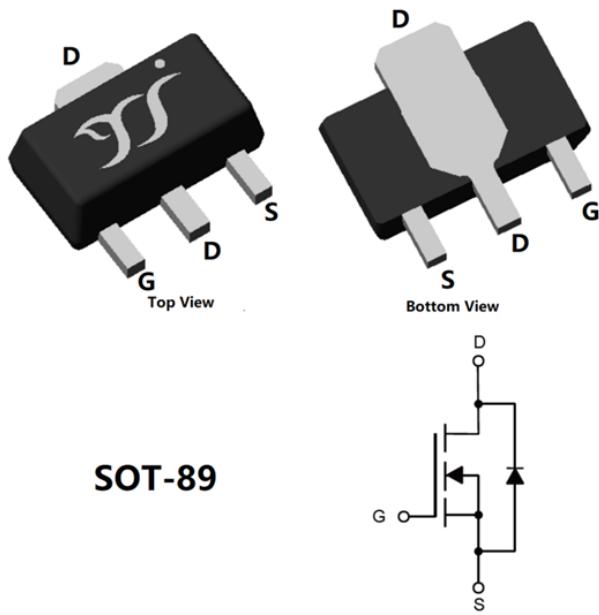




N-Channel Enhancement Mode Field Effect Transistor



Product Summary

| | |
|------------------------------------|-------|
| • V_{DS} | 60V |
| • I_D | 5A |
| • $R_{DS(ON)}$ (at $V_{GS}=10V$) | <31mΩ |
| • $R_{DS(ON)}$ (at $V_{GS}=4.5V$) | <38mΩ |

General Description

- Trench Power LV MOSFET technology
- Excellent package for heat dissipation
- High density cell design for low $R_{DS(ON)}$
- Epoxy Meets UL 94 V-0 Flammability Rating
- Halogen Free

Applications

- Power switching application
- Uninterruptible power supply
- DC-DC convertor

(unless otherwise noted)

| Parameter | | Symbol | Limit | Unit |
|--|-------------------|----------------|----------|------|
| Drain-source Voltage | | V_{DS} | 60 | V |
| Gate-source Voltage | | V_{GS} | ± 20 | V |
| Drain Current | $T_A=25^\circ C$ | I_D | 5 | A |
| | $T_A=100^\circ C$ | | 3 | |
| Pulsed Drain Current ^A | | I_{DM} | 40 | A |
| Total Power Dissipation ^B | $T_A=25^\circ C$ | P_D | 1.78 | W |
| | $T_A=100^\circ C$ | | 0.71 | |
| Junction and Storage Temperature Range | | T_J, T_{STG} | -55~+150 | °C |

■ Thermal resistance

| Parameter | | Symbol | Typ | Max | Units |
|---|--------------|-----------------|-----|-----|-------|
| Thermal Resistance Junction-to-Ambient ^C | Steady-State | $R_{\theta JA}$ | 56 | 70 | °C/W |

■ Ordering Information (Example)

| PREFERRED P/N | PACKING CODE | Marking | MINIMUM PACKAGE(pcs) | INNER BOX QUANTITY(pcs) | OUTER CARTON QUANTITY(pcs) | DELIVERY MODE |
|---------------|--------------|---------|----------------------|-------------------------|----------------------------|---------------|
| YJH05N06A | F2 | 6005A | 1000 | 8000 | 32000 | 7" reel |



YJH05N06A

■ Electrical Characteristics (T_J=25°C unless otherwise noted)

| Parameter | Symbol | Conditions | Min | Typ | Max | Units |
|---------------------------------------|---------------------|--|-----|------|------|-------|
| Static Parameter | | | | | | |
| Drain-Source Breakdown Voltage | BV _{DSS} | V _{GS} =0V, I _D =250μA | 60 | - | - | V |
| Zero Gate Voltage Drain Current | I _{DSS} | V _{DS} =60V, V _{GS} =0V | - | - | 1 | μA |
| | | V _{DS} =60V, V _{GS} =0V, T _j =150°C | - | - | 100 | |
| Gate-Body Leakage Current | I _{GSS} | V _{GS} =±20V, V _{DS} =0V | - | - | ±100 | nA |
| Gate Threshold Voltage | V _{GS(th)} | V _{DS} =V _{GS} , I _D =250μA | 1 | 1.5 | 2.5 | V |
| Static Drain-Source On-Resistance | R _{DS(on)} | V _{GS} =10V, I _D =5A | - | 23 | 31 | mΩ |
| | | V _{GS} =4.5V, I _D =3A | - | 27 | 38 | |
| Diode Forward Voltage | V _{SD} | I _S =5A, V _{GS} =0V | - | - | 1.2 | V |
| Gate resistance | R _G | f=1MHz | - | 1.7 | - | Ω |
| Maximum Body-Diode Continuous Current | I _S | | - | - | 5 | A |
| Dynamic Parameters | | | | | | |
| Input Capacitance | C _{iss} | V _{DS} =30V, V _{GS} =0V, f=1MHz | - | 1150 | - | pF |
| Output Capacitance | C _{oss} | | - | 65 | - | |
| Reverse Transfer Capacitance | C _{rss} | | - | 55 | - | |
| Switching Parameters | | | | | | |
| Total Gate Charge | Q _g | V _{GS} =10V, V _{DS} =30V, I _D =5A | - | 22 | - | nC |
| Gate-Source Charge | Q _{gs} | | - | 3.2 | - | |
| Gate-Drain Charge | Q _{gd} | | - | 4.3 | - | |
| Reverse Recovery Charge | Q _{rr} | I _F =5A, di/dt=100A/us | - | 18 | - | nC |
| Reverse Recovery Time | t _{rr} | | - | 26 | - | ns |
| Turn-on Delay Time | t _{D(on)} | V _{GS} =10V, V _{DD} =30V, I _D =5A R _{GEN} =2.2Ω | - | 8 | - | ns |
| Turn-on Rise Time | t _r | | - | 20 | - | |
| Turn-off Delay Time | t _{D(off)} | | - | 21 | - | |
| Turn-off fall Time | t _f | | - | 2 | - | |

- A. Repetitive rating; pulse width limited by max. junction temperature.
- B. P_d is based on max. junction temperature, using junction-case thermal resistance.
- C. The value of R_{θJA} is measured with the device mounted on 1 in² FR-4 board with 2oz. Copper, in the still air environment with T_A=25°C. The maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.



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■ Typical Electrical and Thermal Characteristics Diagrams

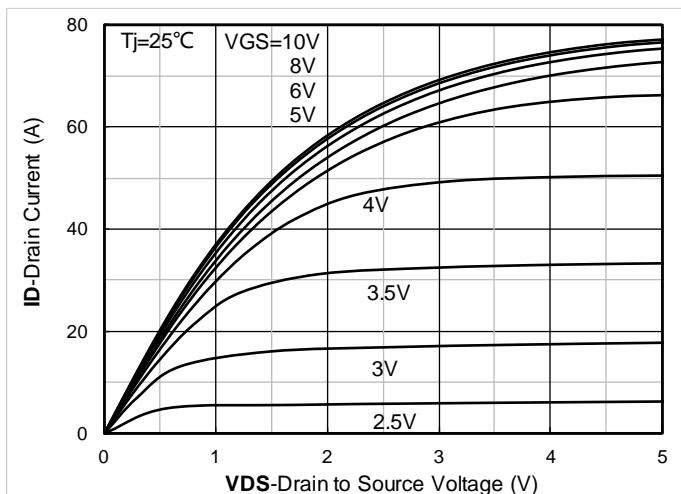


Figure 1. Output Characteristics

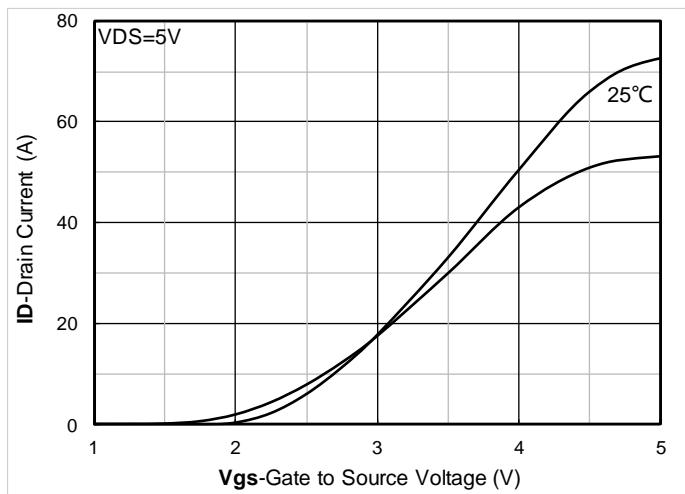


Figure 2. Transfer Characteristics

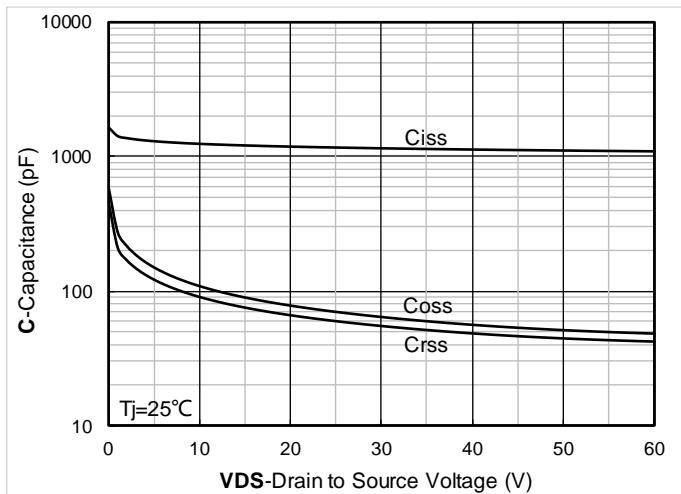


Figure 3. Capacitance Characteristics

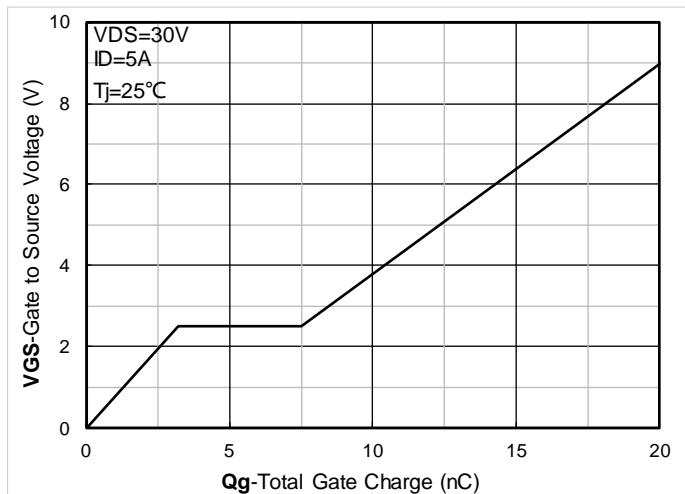


Figure 4. Gate Charge

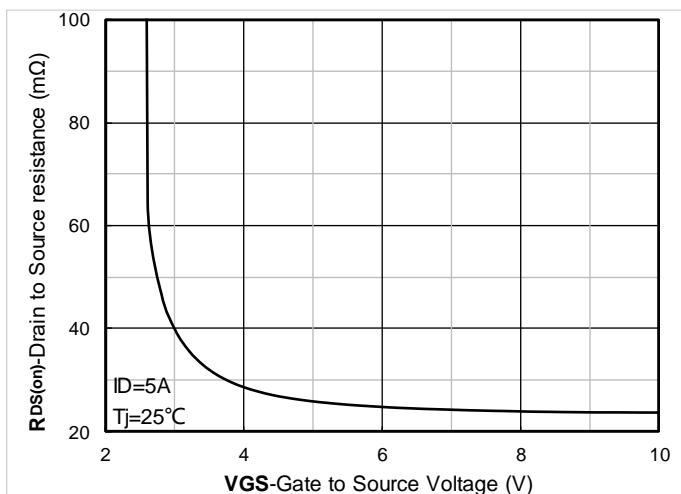


Figure 5. On-Resistance vs Gate to Source Voltage

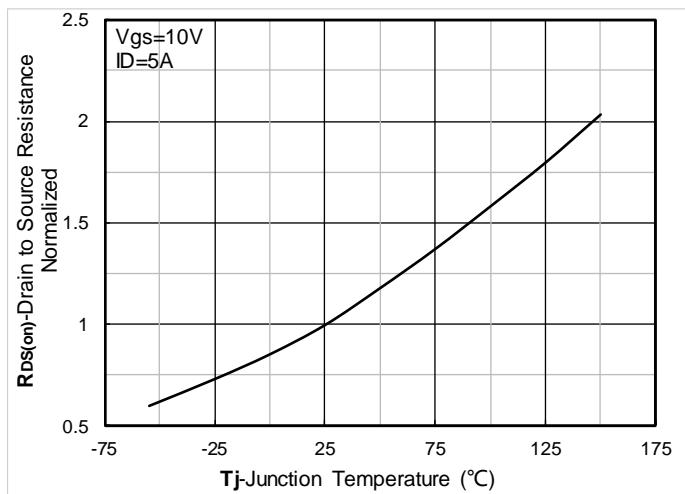


Figure 6. Normalized On-Resistance



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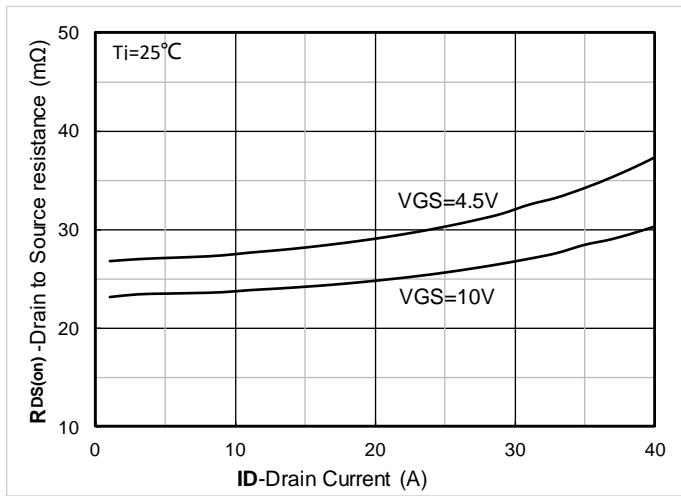


Figure 7. $R_{DS(on)}$ VS Drain Current

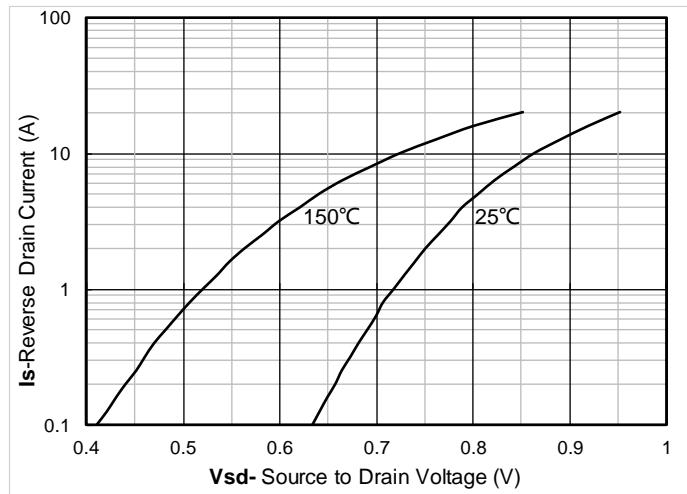


Figure 8. Forward characteristics of reverse diode

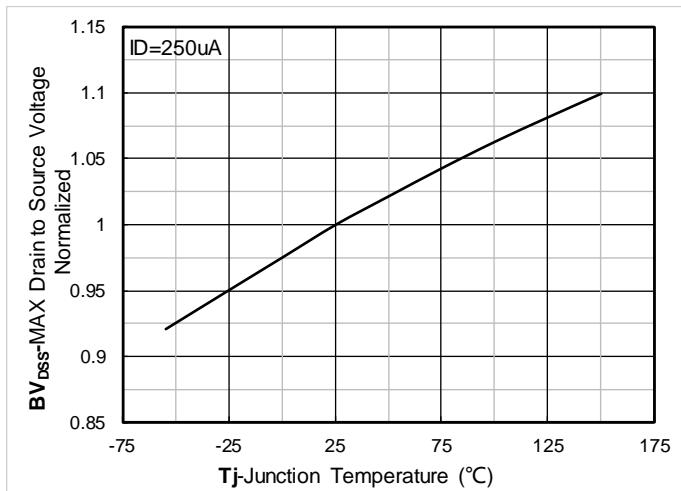


Figure 9. Normalized breakdown voltage

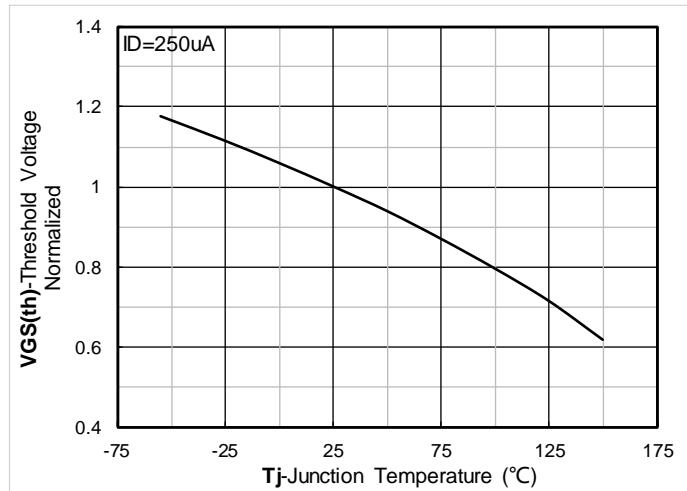


Figure 10. Normalized Threshold voltage

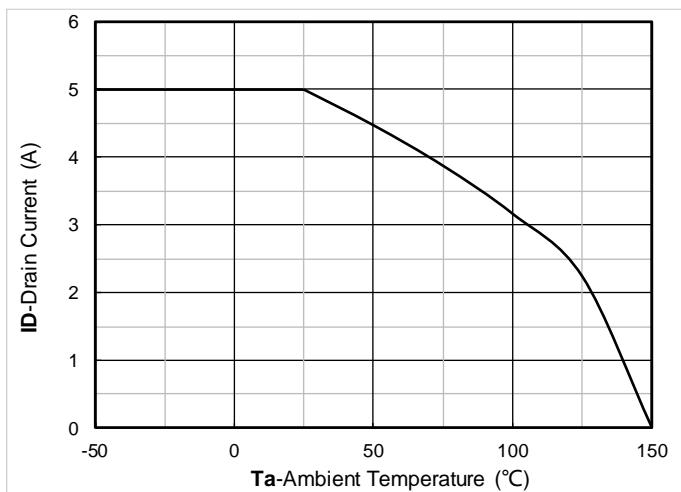


Figure 11. Current dissipation

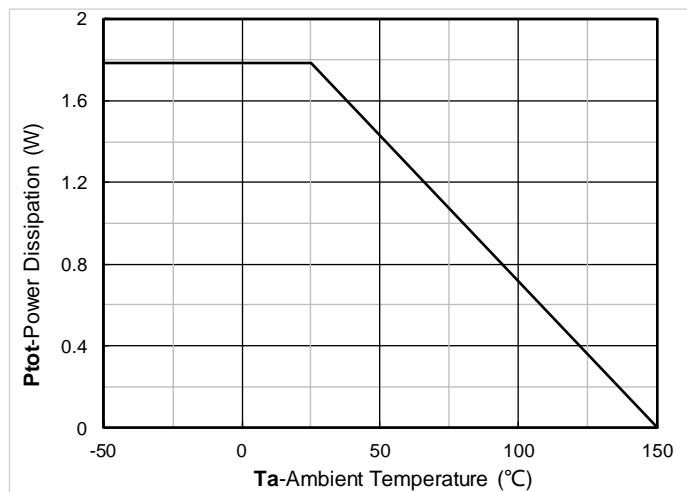


Figure 12. Power dissipation



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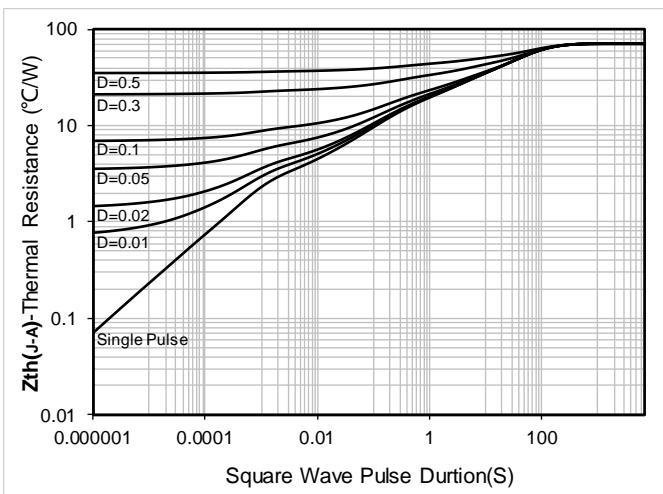


Figure 13. Maximum Transient Thermal Impedance

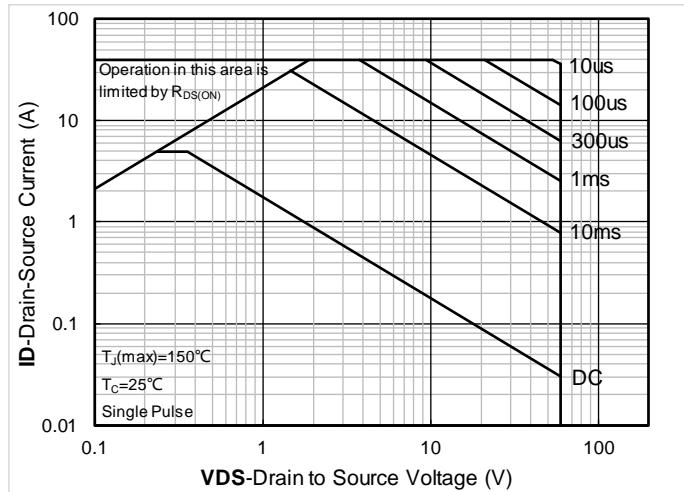
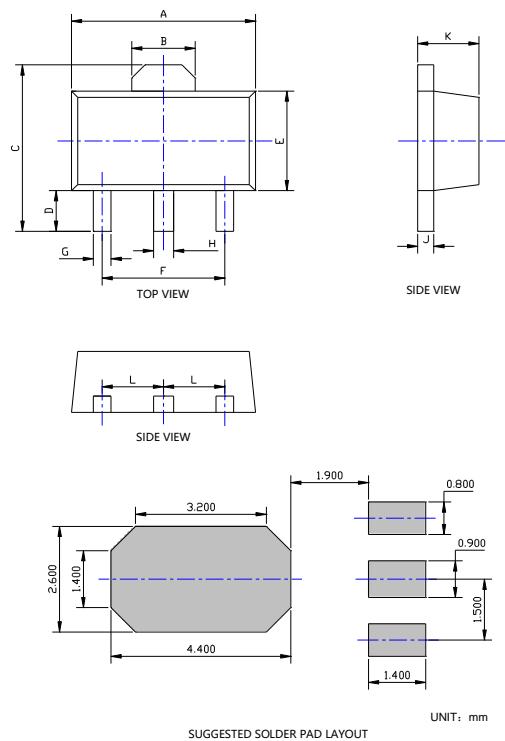


Figure 14. Safe Operation Area

**■ SOT-89 Package information**

| DIM | DIMENSIONS | | | |
|-----|------------|----------|------------|----------|
| | INCHES | | Millimeter | |
| | MIN. | MAX. | MIN. | MAX. |
| A | 0.169 | 0.185 | 4.300 | 4.700 |
| B | | 0.061TYP | | 1.550TYP |
| C | 0.154 | 0.171 | 3.910 | 4.350 |
| D | 0.031 | 0.047 | 0.800 | 1.200 |
| E | 0.089 | 0.104 | 2.250 | 2.650 |
| F | | 0.118TYP | | 3.000TYP |
| G | 0.013 | 0.020 | 0.330 | 0.520 |
| H | 0.016 | 0.023 | 0.400 | 0.580 |
| J | 0.014 | 0.017 | 0.350 | 0.440 |
| K | 0.055 | 0.063 | 1.400 | 1.600 |
| L | | 0.059TYP | | 1.500TYP |

NOTE:

1.PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.
2.TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.

3.THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.



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