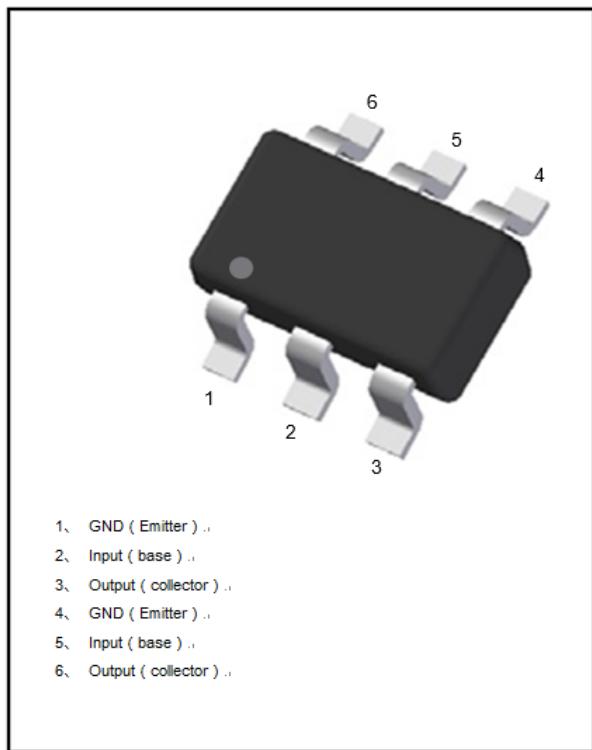


Digital Transistors (Built-in Resistors)



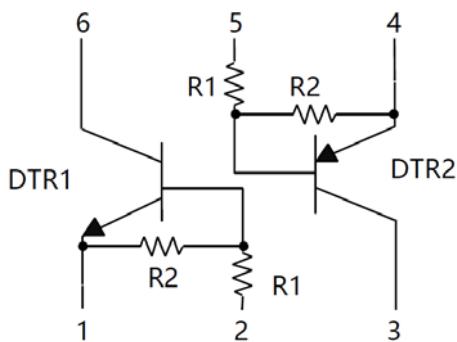
Features

- Epoxy meets UL-94 V-0 flammability rating
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Marking:** D18

■Equivalent circuit



■Maximum Ratings (Ta=25°C Unless otherwise specified)

DTR1-NPN

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V _{CC}	V		50
Input Voltage	V _{IN}	V		-7 to +20
Output Current	I _O	mA		100
Power Dissipation	P _D	mW		150
Junction Temperature (Single)	T _j	°C		150
Storage Temperature	T _{STG}	°C		-55 to +150



UMD18NS

DTR2-PNP

ITEM	SYMBOL	UNIT	CONDITIONS	VALUE
Supply Voltage	V _{CC}	V		-50
Input Voltage	V _{IN}	V		-20 to +7
Output Current	I _O	mA		-100
Power Dissipation	P _D	mW		150
Junction Temperature	T _J	°C		150
Storage Temperature	T _{STG}	°C		-55 to +150

■Electrical Characteristics (Ta=25°C unless otherwise specified)

DTR1-NPN

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	V _{I(off)}	V	V _{CC} =5V, I _C =100uA	0.3		
	V _{I(on)}	V	V _O =0.3V, I _C =20mA			2.5
Output voltage	V _{O(on)}	V	I _O /I _I =10mA/0.5 mA			0.3
Input current	I _I	mA	V _I =5V			1.8
Output current	I _{O(off)}	uA	V _{CC} =50V, V _I =0			0.5
DC current gain	G _I		V _O =5V, I _O =10mA	30		
Input resistance	R ₁	kΩ		3.29	4.7	6.11
Resistance ratio	R ₂ /R ₁			1.7	2.1	2.6
Transition frequency	f _T	MHz	V _{CE} =10V, I _E =5mA, f=100MHz		250	

DTR2-PNP

ITEM	SYMBOL	UNIT	CONDITIONS	MIN	TYP	MAX
Input voltage	V _{I(off)}	V	V _{CC} =-5V, I _C =-100uA	-0.3		
	V _{I(on)}	V	V _O =-0.3V, I _C =-20mA			-2.5
Output voltage	V _{O(on)}	V	I _O /I _I =-10mA/-0.5 mA			-0.3
Input current	I _I	mA	V _I =-5V			-1.8
Output current	I _{O(off)}	uA	V _{CC} =-50V, V _I =0			-0.5
DC current gain	G _I		V _O =-5V, I _O =-10mA	30		
Input resistance	R ₁	kΩ		3.29	4.7	6.11
Resistance ratio	R ₂ /R ₁			1.7	2.1	2.6
Transition frequency	f _T	MHz	V _{CE} =-10V, I _E =-5mA, f=100MHz		250	

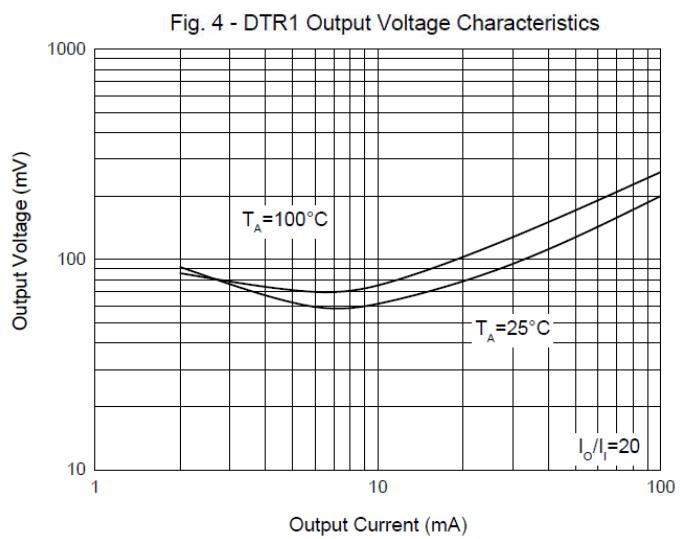
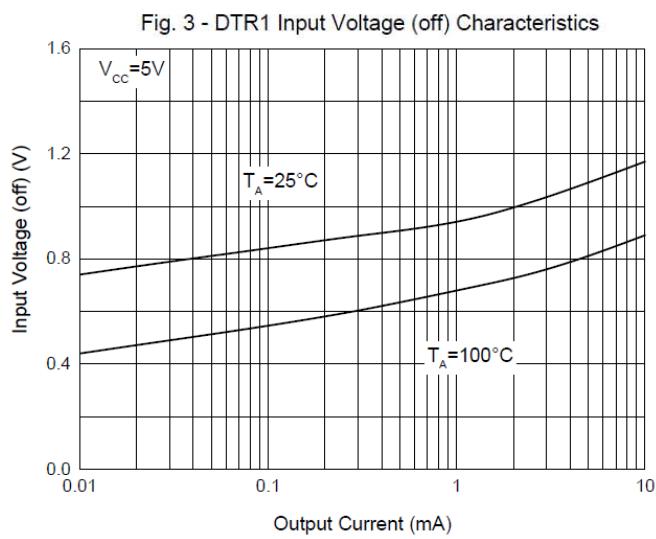
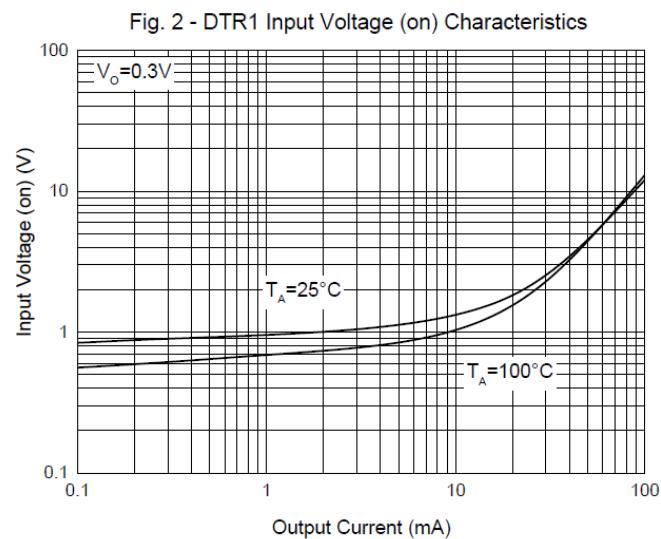
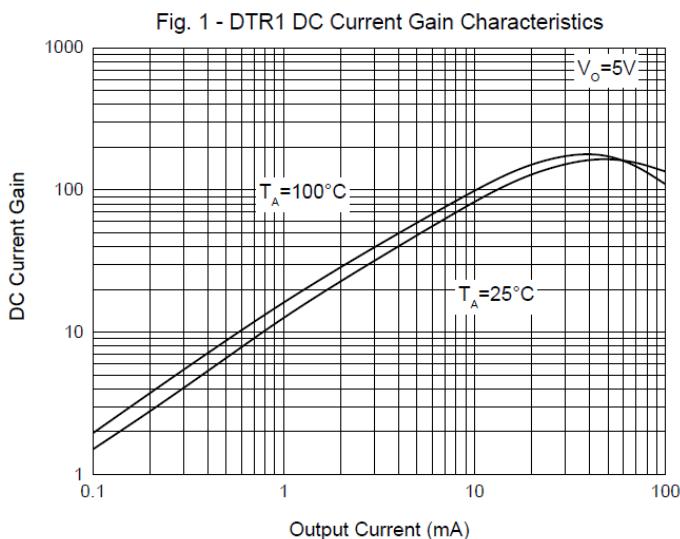


UMD18NS

■ Ordering Information (Example)

PREFERRED P/N	PACKING CODE	UNIT WEIGHT(g)	MINIMUM PACKAGE(pcs)	INNER BOX QUANTITY(pcs)	OUTER CARTON QUANTITY(pcs)	DELIVERY MODE
UMD18NS	F2	Approximate 0.009g	3000	30000	120000	7" reel

■ Characteristics (Typical)





UMD18NS

Fig. 5 - DTR2 DC Current Gain Characteristics

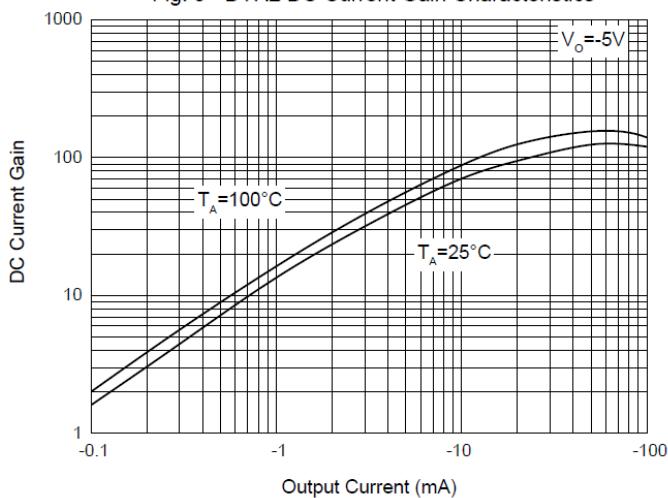


Fig. 6 - DTR2 Input Voltage (on) Characteristics

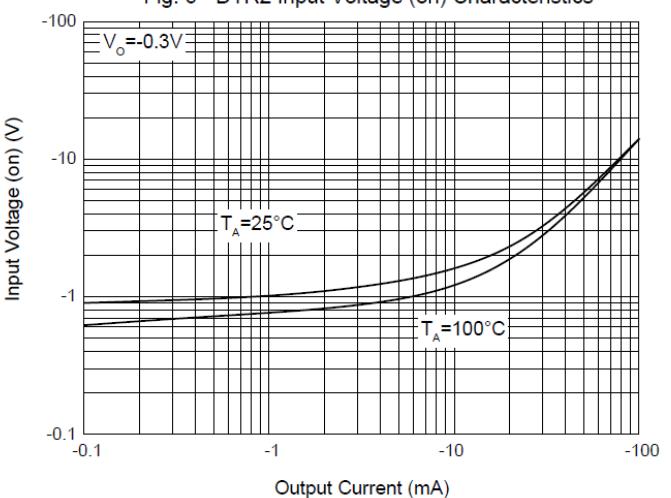


Fig. 7 - DTR2 Input Voltage (off) Characteristics

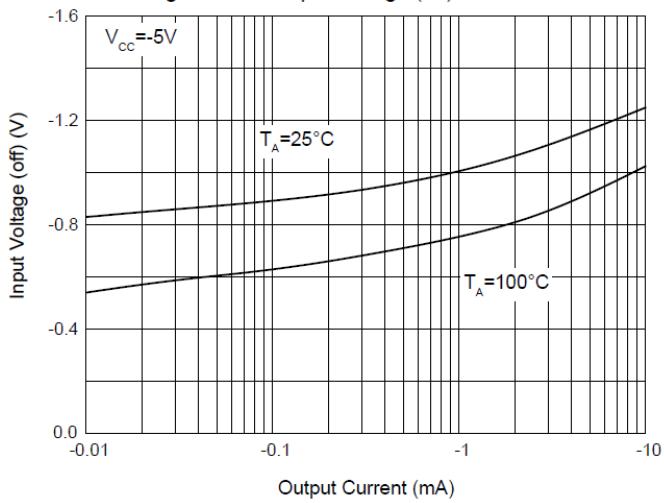
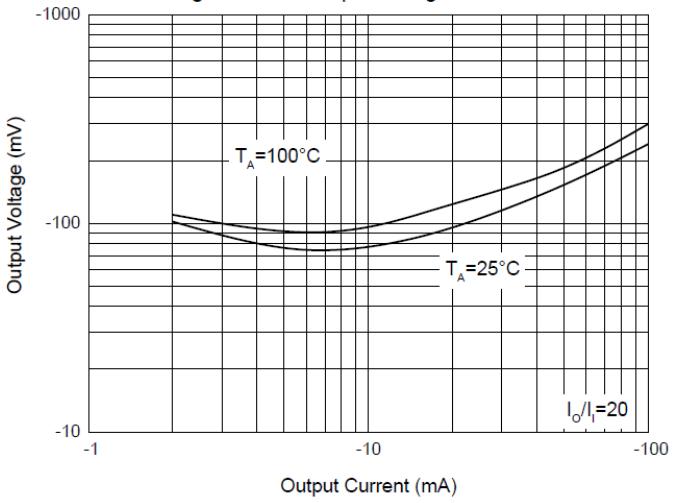
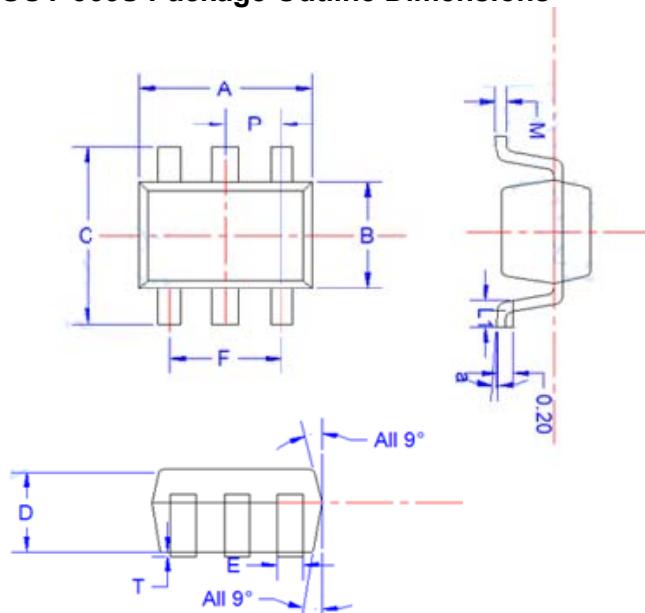
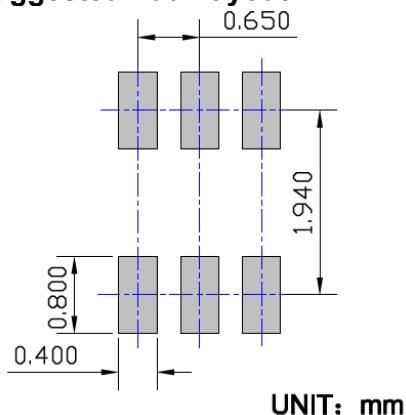


Fig. 8 - DTR2 Output Voltage Characteristics



**■SOT-363S Package Outline Dimensions**

SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	0.15	0.25	0.35
B	1.15	1.25	1.35
C	2.00	2.10	2.20
P	0.650BSC		
A	1.80	2.00	2.20
T	0.00	0.05	0.100
D	0.90	0.95	1.00
L1	0.20	0.30	0.40
a	4°±4°		
M	0.10	0.15	0.25

■Suggested Pad Layout



Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function or design or otherwise.

The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website <http://www.21yangjie.com>, or consult your nearest Yangjie's sales office for further assistance.