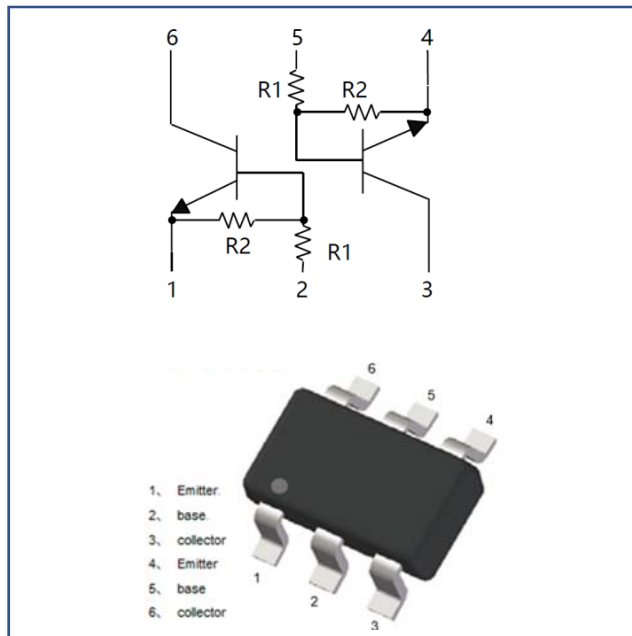


Dual NPN Digital Transistors (Built-in Resistors)



Features

- Moisture sensitivity level 1
- Halogen free and RoHS compliant
- Surface mount package ideally suited for automatic Insertion

Application

- Signal amplification
- Switching circuit

Mechanical data

- **Package:** SOT-363S
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102

■ Maximum Ratings ($T_a=25^{\circ}\text{C}$ Unless otherwise specified)

Item	Symbol	Unit	Conditions	Value
Device marking code				H18
Collector-base voltage	V_{CC}	V		50
Collector-emitter voltage	V_{IN}	V		-7 to +20
Collector current	I_o	mA		100
Power dissipation	P_D	mW		150
Operation junction temperature	T_J	$^{\circ}\text{C}$		-55 to +150
Storage temperature	T_{STG}	$^{\circ}\text{C}$		-55 to +150



UMH18NS

■ Electrical Characteristics (T_a=25°C Unless otherwise specified)

Item	Symbol	Unit	Conditions	Min	Typ	Max
Input voltage	V _{I(off)}	V	V _{CC} =5V, I _c =100uA	0.3		
	V _{I(on)}	V	V _o =0.3V, I _c =20mA			2.5
Output voltage	V _{O(on)}	V	I _o / I _i =10mA/0.5 mA			0.3
Input current	I _i	mA	V _i =5V			0.6
Output current	I _{O(off)}	uA	V _{CC} =50V, V _i =0			0.5
DC current gain	G _i		V _o =5V, I _o =10mA	50		
Input resistance	R ₁	kΩ		3.3	4.7	6.1
Resistance ratio	R ₂ /R ₁			1.7	2.1	2.6
Transition frequency	f _T	MHz	V _o =10V, I _o =5mA, f=100MHz		250	

■ Thermal Characteristics

Parameter	Symbol	Unit	Value
Thermal resistance, junction-to-ambient	R _{θJ-A} ⁽¹⁾	°C/W	834
Thermal resistance, junction-to-case	R _{θJ-C} ⁽¹⁾	°C/W	667

Note:

(1) Device mounted on PCB, single-sided copper, with standard footprint

■ Ordering Information

Part number	Packing code	Unit weight(g)	Minimum package(pcs)	Inner box quantity(pcs)	Outer carton quantity(pcs)	Delivery mode
UMH18NS	F2	Approximate 0.009	3000	30000	120000	7" reel
UMH18NS	F3	Approximate 0.009	10000		210000	7" reel



■ Characteristics

Fig 1: Input Voltage (On) Characteristics

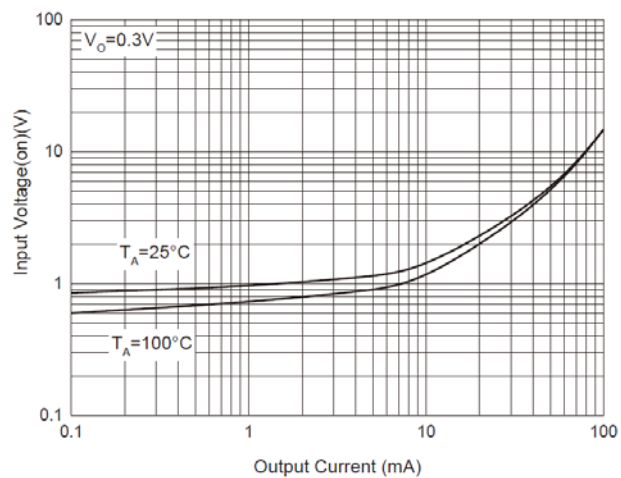


Fig 2: Input Voltage (Off) Characteristic

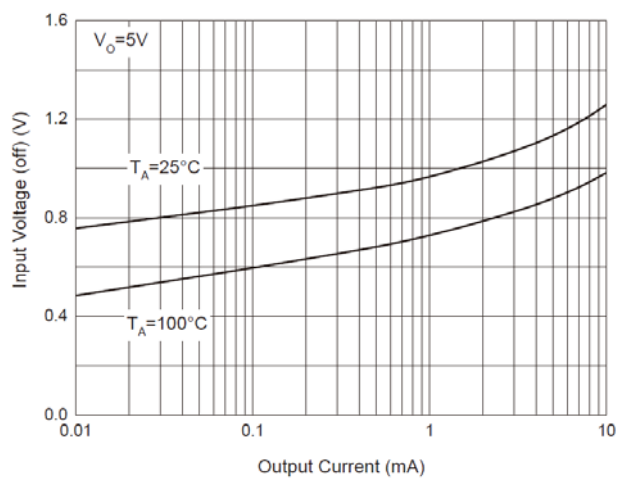


Fig 3: DC Current Gain Characteristics

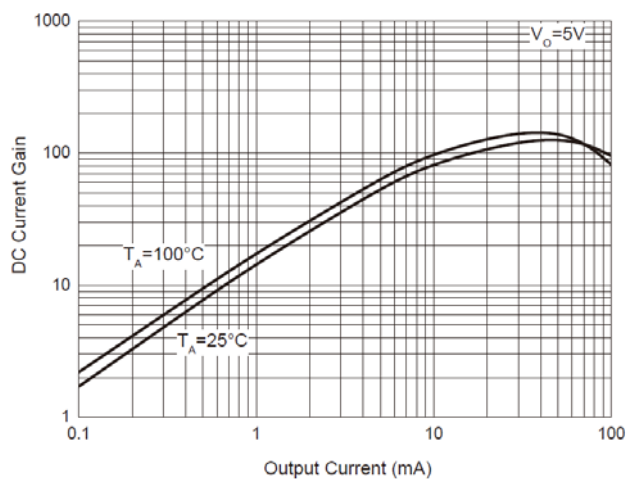


Fig 4: Output Voltage Characteristics

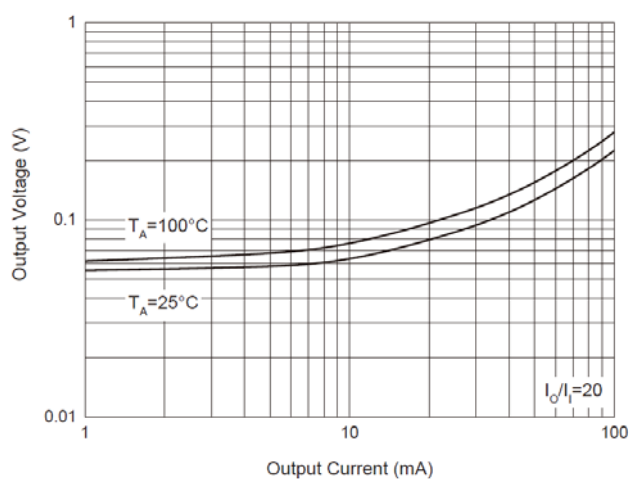
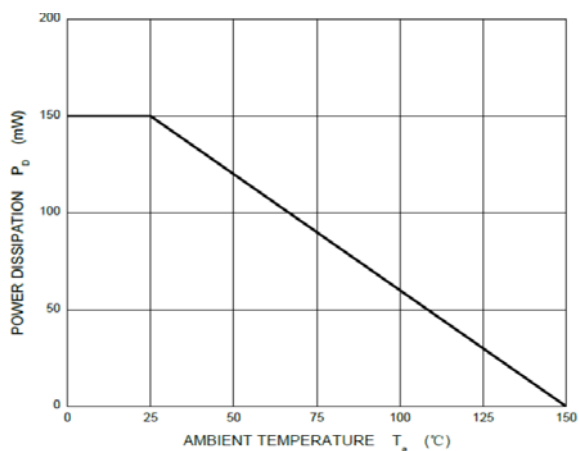


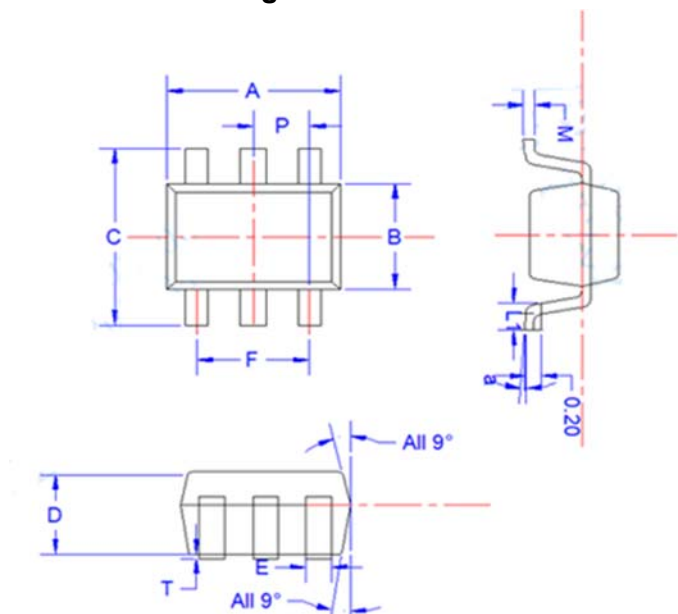
Fig 5: P_D - T_A Curve





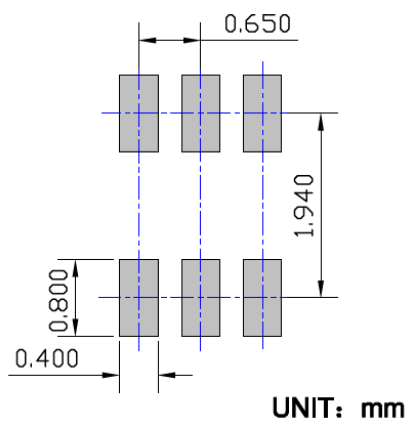
UMH18NS

■SOT-363S Package Outline Dimensions



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
E	0.15	0.25	0.35
B	1.15	1.25	1.35
C	2.00	2.10	2.20
P	0.650BSC		
A	1.80	2.00	2.20
T	0.00	0.05	0.100
D	0.90	0.95	1.00
L1	0.20	0.30	0.40
a	4°±4°		
M	0.10	0.15	0.25

■Suggested Pad Layout





UMH18NS

Disclaimer

The information presented in this document is for reference only. Yangzhou Yangjie Electronic Technology Co., Ltd. reserves the right to make changes without notice for the specification of the products displayed herein to improve reliability, function, or design or otherwise.

The product listed herein is designed to be used with ordinary electronic equipment or devices, and not designed to be used with equipment or devices which require high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), Yangjie or anyone on its behalf, assumes no responsibility or liability for any damages resulting from such improper use of sale.

This publication supersedes & replaces all information previously supplied. For additional information, please visit our website [http:// www.21yangjie.com](http://www.21yangjie.com) , or consult your nearest Yangjie's sales office for further assistance.